Assignment 12

Multiplexors

Introduction

Use LogicCircuit to create a 16:1 1-bit multiplexor.

The Circuit

Implement the multiplexor recursively, starting with a 2:1 1-bit multiplexor. Create the following circuits:

- 2:1 1-bit multiplexor (`MUX 2:1 (1)`)
- 4:1 1-bit multiplexor (`MUX 4:1 (1)`)
- 8:1 1-bit multiplexor (`MUX 8:1 (1)`)
- 16:1 1-bit multiplexor (`MUX 16:1 (1)`)

The sixteen 1-bit inputs, labeled `0` through `F`, should be on the left, the select input should be at the bottom, and the 1-bit output should be on the right.

Due Date

Save your circuits in one file named `Multiplexor.CircuitProject`. Place it in a folder named `Assignment 12` and drop it in the dropbox. It is due by midnight Wednesday, October 30.